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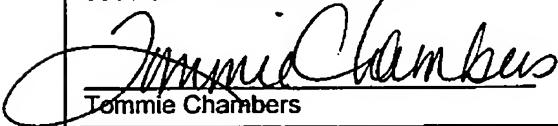
From: W. Daniel Swayze, Jr.
Texas Instruments Incorporated
Facsimile: 972-917-4418
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NAME OF INVENTOR(S): Barnett	
TITLE OF INVENTION: CIRCUIT AND METHOD TO COMPENSATE FOR RMR VARIATIONS AND FOR SHUNT RESISTANCE ACROSS RMR IN AN OPEN LOOP CURRENT BIAS ARCHITECTURE	
TI FILE NO.:	DEPOSIT ACCT. NO.:
TI-36061	20-0668
FAXED: 07/16/2004	
DUE: 07/29/2004	
ATTY/SECY: wds/tc	
RECEIPT DATE & SERIAL NO.: Serial No.: 10/696,827 Filing Date: 10/30/2003	

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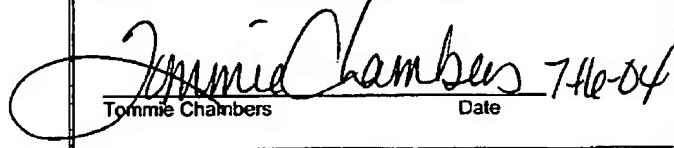
Applicant: Barnett Docket No: TI-36061
Serial No: 10/696,827 Examiner: Wells, Kenneth B.
Filed: 10/30/2003 Art Unit: 2816
For: CIRCUIT AND METHOD TO COMPENSATE FOR RMR VARIATIONS
AND FOR SHUNT RESISTANCE ACROSS RMR IN AN OPEN LOOP
CURRENT BIAS ARCHITECTURE

RESPONSE

Assistant Commissioner For Patents
Washington, DC 20231

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Dear Sir:

Responsive to the Office Action mailed June 29, 2004 in connection with the
above identified application, Applicant(s) respectfully submit(s) the following remarks.

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